



95/IFW 2876

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re original application of:

Applicant : Timothy Good
Serial No. : 10/045,577 ✓
Filed : January 11, 2002
Title of Invention : BIOPTICAL POINT-OF-SALE (POS) SCANNING SYSTEM
EMPLOYING DUAL POLYGON-BASED LASER SCANNING
PLATFORMS DISPOSED BENEATH HORIZONTAL AND
VERTICAL SCANNING WINDOWS FOR 360°...
Examiner : Thien Le
Group Art Unit : 2876
Attorney Docket : 108-120USA000

Honorable Commissioner of Patents
and Trademarks
Washington, DC 20231

RESPONSE TO NOTICE REGARDING DRAWINGS

Sir:

In response to the Notice Regarding Drawings mailed in the present Application on December 9, 2004, Applicant submits the following documents:

- Amendment;
- Request for Approval of Proposed Changes to the Drawings;
- Five (5) sheets of Redmarked Drawings;
- Submission of Formal Drawings;
- Ninety One (91) sheets of Formal Drawings; and
- Return Receipt Postcard.

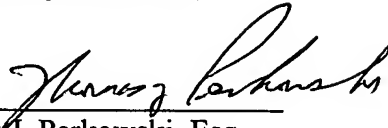
The Commissioner is hereby authorized to charge any fee deficiencies in connection with the present Application to Deposit Account 16-1340.

Please address all correspondence to the undersigned attorney at the following address:

Thomas J. Perkowski, Esq., P.C.
Soundview Plaza
1266 East Main Street
Stamford, Connecticut 06902

Respectfully submitted,

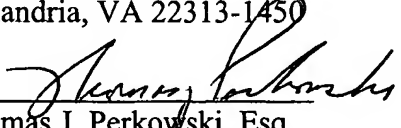
Dated: December 29, 2004

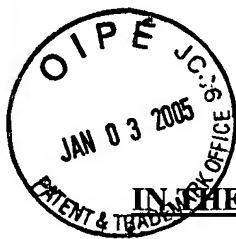

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Thomas J. Perkowski, Esq.
Dated: December 29, 2004



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AMENDMENT

Sir:

Prior to examination of the above-referenced Patent Application, please amend the same as follows:

AMENDMENT TO THE SPECIFICATION:

On Page 27, amend the first and last paragraphs as follows:

Fig. 11 is Figs. 11A and 11B, taken together, set forth a schematic diagram illustrating an exemplary embodiment of the signal conditioning circuitry 903 of Fig. 9, which operates to amplify and smooth out or otherwise filter the scan data signal produced by the photodetector 902 to remove unwanted noise components therein, including a number of subcomponents arranged in a serial manner, namely: a high gain amplifier stage 1103, a multistage amplifier stage 1105, a differential amplifier stage 1107 and a low pass filter (LPF) stage 1109.

Fig. 14 is Figs. 14A through 14C set forth a schematic diagram illustrating an exemplary implementation of the first derivative signal threshold circuitry 905, which is suitable for use in the two different paths of the scan data signal processor of Fig. 9, including: an amplifier stage 1401 that amplifies the voltage levels of the first derivative signal produced by the first derivative signal generation circuitry 904, positive and negative peak detectors 1403 and 1405,